

⁽¹²⁾ UK Patent Application ⁽¹⁹⁾ GB ⁽¹¹⁾ 2 338 849 ⁽¹³⁾ A

(43) Date of A Publication 29.12.1999

(21) Application No 9813585.8

(22) Date of Filing 24.06.1998

(71) Applicant(s)
Motorola, Inc
(Incorporated in USA - Delaware)
1303 East Algonquin Road, Schaumburg,
Illinois 60196, United States of America

(72) Inventor(s)
Jean-Daniel Frund

(74) Agent and/or Address for Service
Sarah Gibson
Motorola Limited, European Intellectual
Property Department, Midpoint, Alencon Link,
BASINGSTOKE, Hampshire, RG21 7PL,
United Kingdom

(51) INT CL⁶
H03B 5/12

(52) UK CL (Edition Q)
H3F FKX
H3T T1G3X T2Q T2T2X T2T3B T3N T3P T3V

(56) Documents Cited
EP 0198550 A1 US 5486794 A

(58) Field of Search
UK CL (Edition Q) H3F FJAA FJAB FKP FKX , H3R
RFMA
INT CL⁶ H03B 1/00 5/00 5/02 5/04 5/08 5/12 5/18
ONLINE:WPI,EPODOC.PAJ

(54) Abstract Title
Cascode, differential and cross coupled VCO with reduced noise

(57) A Voltage Controlled Oscillator circuit has a resonant tank circuit 130 and a differential pair of transistors 110, 120 coupled to drive the tank circuit. A differential cascode pair 140, 150 is provided, coupled between the differential pair and the tank circuit. A differential feedback path is also provided, which is cross-coupled from the tank circuit to drive the differential pair. Base terminals of the cascode pair are AC cross-coupled with respect to the differential pair such that AC base currents of the differential pair and the cascode pair are cross-compensated. This is said to improve noise performance.

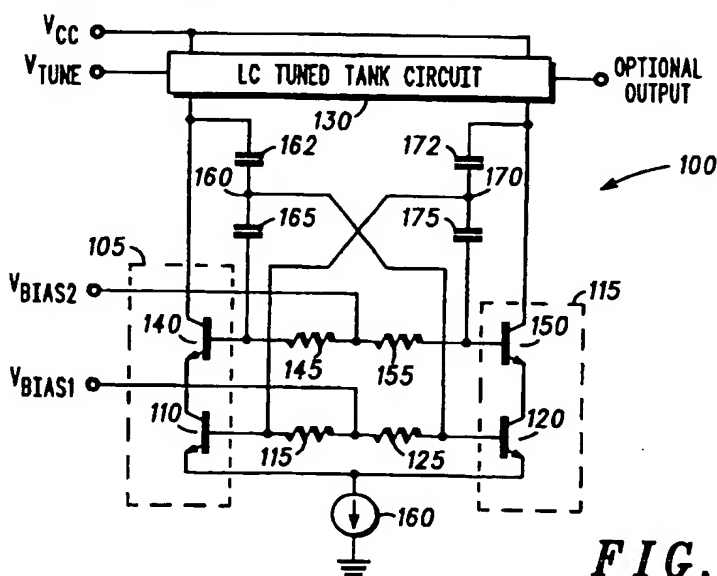
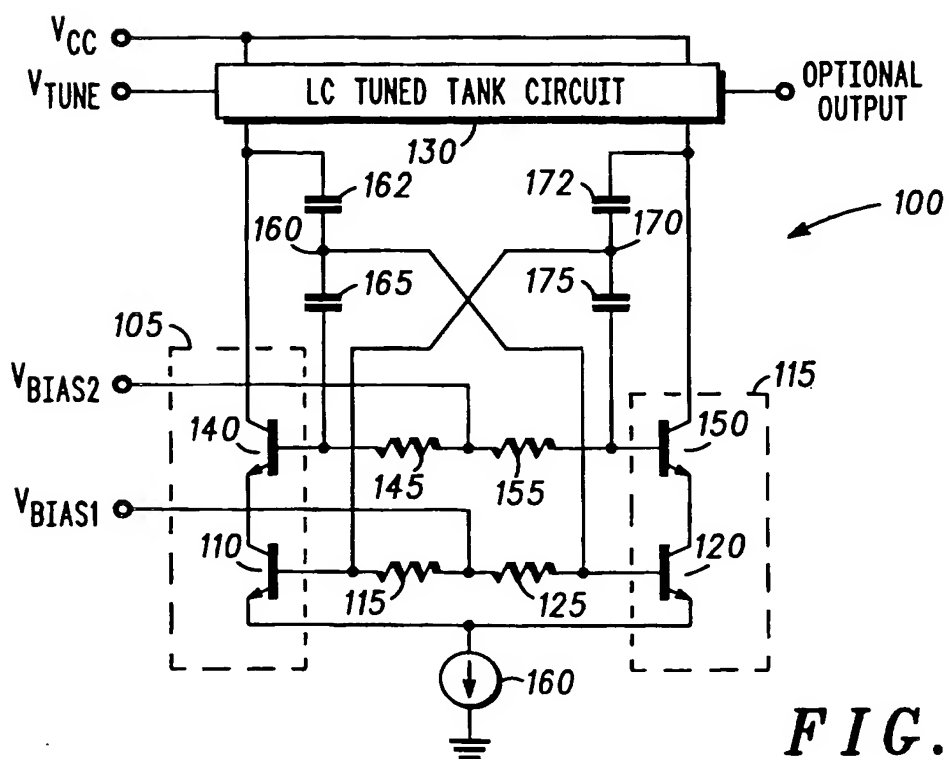
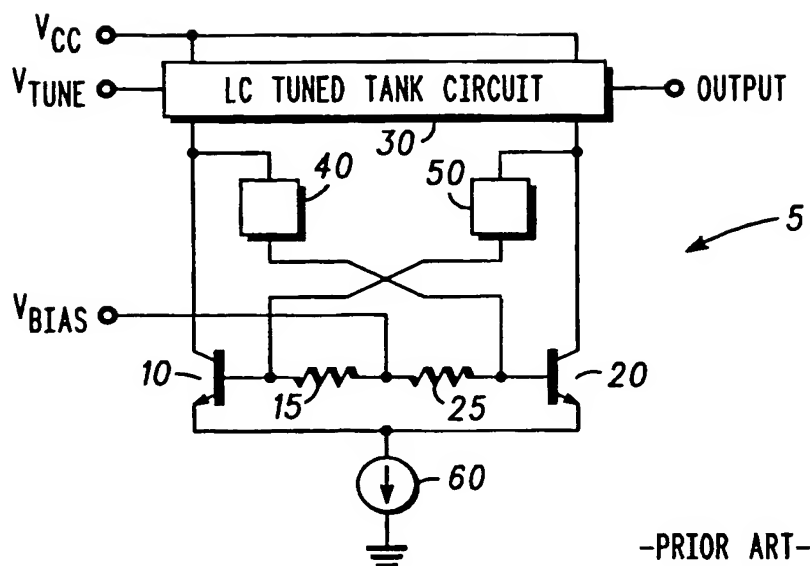


FIG. 2

GB 2 338 849 A



VOLTAGE CONTROLLED OSCILLATOR CIRCUIT

Field of the Invention

- 5 This invention relates to Voltage Controlled Oscillator (VCO) circuits and particularly but not exclusively to VCO's with resonator circuits.

Background of the Invention

- 10 VCO's with L-C resonator (or tank) circuits are used in many mixer-oscillator applications such as terrestrial and satellite television receivers, wireless communications and the like.

15 An example of such a VCO is shown in FIG. 1, which depicts a prior art emitter-coupled oscillator 5, having first and second emitter-coupled transistors 10 and 20 respectively coupled via their collector terminals to two terminals of a tank circuit 30. First and second feedback paths 40 and 50 respectively are cross-coupled between the two terminals of the tank circuit 30 and base terminals of the first and second transistors 10 and 20 respectively. The emitter terminals of the first and second transistors 10 and 20 respectively are coupled to a current source 60. A bias voltage is applied to the base terminals of the first and second transistors 10 and 20 via first and second biasing resistors 15 and 25 respectively.

- 25 The circuit of FIG.1 allows a reasonably high amplitude of oscillation, but suffers from a low effective quality factor (as load effects increase the effective resistance of the circuit), and from phase perturbation, caused by base currents associated with the capacitive couplings. Furthermore, the first and second emitter-follower transistors 10 and 20 give rise to the
30 unwanted effects of a large noise factor and open loop phase error.

Other circuits such as a cascode arrangement are known, which provide advantages over the circuit of FIG. 1 in terms of the quality factor.

However, such circuits again suffer from open loop phase error, noise contribution due to the additional transistors in the feedback paths (increasing the phase noise) and increased power consumption.

- 5 This invention seeks to provide a VCO circuit which mitigates the above mentioned disadvantages.

Summary of the Invention

10

According to the present invention there is provided a VCO circuit, comprising: a resonant tank circuit; a differential pair of transistors, coupled to drive the tank circuit; a differential cascode pair, coupled between the differential pair and the tank circuit, the cascode pair having
15 base terminals and, a differential feedback path, cross-coupled from the tank circuit to drive the differential pair, wherein the base terminals of the cascode pair are AC cross-coupled with respect to the differential pair such that AC base currents of the differential pair and the cascode pair are cross-compensated.

20

Preferably, the differential feedback path includes first DC blocking capacitors. The base terminals of the differential cascode are preferably AC cross-coupled to the differential pair by means of second DC blocking capacitors, such that the cascodes act as diodes for the AC signal.

25

Preferably the VCO circuit further comprises biasing resistors coupled to bias the base terminals of the cascode pair, and preferably also further comprises an output amplifier comprising a supplementary differential pair and a supplementary cascode, each coupled to the differential pair and
30 the cascode respectively, for providing a differential amplified output.

The supplementary differential pair and the supplementary cascode are preferably coupled to the differential pair and the cascode via their base

terminals respectively. Preferably the differential amplified output is provided at collector terminals of the supplementary cascode.

10 In this way a low phase noise and low power VCO circuit is provided by
5 having a good effective quality factor (increased input and output impedances of the amplifier), low noise figure of the amplifier (no additional transistor in the feedback paths), reduced open loop phase error (negligible time delay in the feedback paths) and high amplitude of oscillation.

Brief Description of the Drawings

15 An exemplary embodiment of the invention will now be described with reference to the drawing in which:

FIG.1 shows a prior art VCO circuit; and

20 FIG. 2 shows a preferred embodiment of a VCO circuit in accordance with the invention.

Detailed Description of a Preferred Embodiment

25 Referring to FIG. 2, there is shown a VCO circuit 100, having first and second cascode two-transistor subcircuit of transistors 105 and 115 respectively, coupled to a terminals of a tank circuit 130.

30 The first cascode two-transistor subcircuit 105 has a first transistor 110, which has an emitter terminal coupled to a current source 160, a base terminal coupled to a first voltage bias VBIAS1 via a first biasing resistor 115, and a collector terminal to be further described below. The first cascode pair 105 also has a second transistor 140, which has an emitter terminal coupled to the collector terminal of the first transistor 110, a base

terminal coupled to a second voltage bias VBIAS2 via a second biasing resistor 145, and a collector terminal coupled to a first terminal of the tank circuit 130.

5 The second cascode two-transistor subcircuit 115 has a third transistor 120, which has an emitter terminal coupled to the current source 160, a base terminal coupled to the first voltage bias VBIAS1 via a third biasing resistor 125, and a collector terminal to be further described below. The second cascode pair 115 also has a fourth transistor 150, which has an emitter
10 terminal coupled to the collector terminal of the third transistor 120, a base terminal coupled to the second voltage bias VBIAS2 via a fourth biasing resistor 155, and a collector terminal coupled to a second terminal of the tank circuit 130.

15 In this way the first and third transistors 110 and 120 respectively, form a differential pair which drive the tank circuit 130, and the second and fourth transistors 140 and 150 form a differential cascode pair, which act as cascodes with respect to the differential pair (transistors 110 and 120).

20 A first feedback path is formed between the first terminal of the tank circuit 130 and the base terminal of the third transistor 120 via a first node 160. A first DC blocking capacitor 162 is coupled between the first terminal of the tank circuit and the first node 160. The first node 160 is also coupled to the base terminal of the second transistor 140 via a second DC blocking
25 capacitor 165.

A second feedback path is formed between the second terminal of the tank circuit 130 and the base terminal of the first transistor 110 via a second node 170. A third DC blocking capacitor 172 is coupled between the second
30 terminal of the tank circuit and the second node 170. The second node 170 is also coupled to the base terminal of the fourth transistor 150 via a fourth DC blocking capacitor 175.

In this way the first and second feedback paths form a cross-coupled differential feedback path to the differential pair (transistors 110 and 120), and the cascode pair (transistors 140 and 150) are AC cross-coupled with respect to the differential pair (transistors 110 and 120).

5

In operation, the oscillation frequency is set by inductive and capacitive values of the tuned tank circuit. Voltage variable capacitors (VVCs) or varactors provide tuning of the tank circuit 30. The ratio of the maximum to minimum total capacitance associated with the VVC and all the parasitic capacitances connected to the tank circuit set the tuning frequency range of the VCO 100.

10

15

The centre frequency of the VCO 100 is controlled by a DC voltage connected to VTUNE which modulate the total capacitance value of the tank circuit 30.

The differential pair made by transistors 110, 120, the current source 160 and the tank circuit 30 provide the necessary gain and limiting conditions for stable oscillation of the VCO 100.

20 It is noted that an AC coupling achieved by way of a capacitor and separate current sources may be preferred to the DC coupling in the differential pair between the emitters of transistors 110 and 120 to provide rejection of low frequency spurious signals and low frequency noise from the current sources and ensure oscillation in the frequency range of the VCO 100. In addition, the oscillation amplitude may be set by an automatic gain control circuit (AGC) to remove AM components and low frequency modulation of the bias voltage controlling the current sources.

25

30 The AC base and collector voltages of the second transistor 140 and fourth transistor 150 are approximately equal. These transistors 140 and 150 act as diodes at the oscillation frequency and cascodes for low frequencies. Since the noise contribution of the cascodes can be neglected, these transistors can be relatively small compared to the first transistor 100 and

third transistor 120. This serves to reduce the parasitic capacitances on the tank circuit 30 and improve the tuning frequency range.

5 The capacitive couplings associated with the first and second feedback paths allow high amplitude oscillation on the tank circuit 30. Furthermore, the AC base currents of the differential pair made by transistors 110 and 120 flowing through the third and first capacitors 172 and 162 are compensated at nodes 170 and 160 respectively by the AC base currents of transistor 150 and transistor 140 flowing through capacitor 175
10 and capacitor 165 respectively. Therefore, the AC base currents in the positive feedback paths made by capacitors 162 and 172 are reduced, giving rise to a low circuit noise factor.

15 In this way the cross-coupled arrangement, the differential pair, the cascode pair and the feedback paths reduce the phase noise by reducing the noise contribution of the base currents of the differential pair in the feedback paths.

20 It will be appreciated that alternative embodiments to the one described above are possible. For example, an output amplifier comprising a supplementary differential pair and a supplementary cascode could be coupled to the differential pair and the cascode respectively, for providing a differential amplified output. In such an arrangement the supplementary
25 differential pair and the supplementary cascode would be coupled to the differential pair and the cascode via their base terminals respectively, and the differential amplified output would be provided at collector terminals of the supplementary cascode.

30 In this way the tank circuit 130 would not need to be provided with an output to derive the oscillations, but these may instead be sensed from the output amplifier.

Claims

1. A VCO circuit, comprising:
 - 5 a resonant tank circuit;
a differential pair of transistors, coupled to drive the tank circuit;
a differential cascode pair, coupled between the differential pair and the
tank circuit, the cascode pair having base terminals and,
a differential feedback path, cross-coupled from the tank circuit to drive the
10 differential pair,
wherein the base terminals of the cascode pair are AC cross-coupled with
respect to the differential pair such that AC base currents of the differential
pair and the cascode pair are cross-compensated.
- 15 2. The VCO circuit as set forth in claim 1 wherein the differential feedback
path includes first DC blocking capacitors.
3. The VCO circuit as set forth in claim 1 or claim 2 wherein the base
terminals of the differential cascode are AC cross-coupled to the differential
20 pair by means of second DC blocking capacitors, such that the cascodes act
as diodes for the AC signal.
4. The VCO circuit as set forth in any preceding claim further comprising
biasing resistors coupled to bias the base terminals of the cascode pair.
25
5. The VCO circuit as set forth in any preceding claim further comprising
an output amplifier comprising a supplementary differential pair and a
supplementary cascode, each coupled to the differential pair and the
cascode respectively, for providing a differential amplified output.
30
6. The VCO circuit as set forth in claim 5, wherein the supplementary
differential pair and the supplementary cascode are coupled to the
differential pair and the cascode via their base terminals respectively.

7. The VCO circuit as set forth in claim 5, wherein the differential amplified output is provided at collector terminals of the supplementary cascode.

5

8. A VCO circuit substantially as hereinbefore described and with reference to the circuit of FIG. 2.



Application No: GB 9813585.8
Claims searched: 1-8

Examiner: D Midgley
Date of search: 24 February 1999

Patents Act 1977
Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.Q): H3F FKX,FKP,FJAA,FJAB H3R RFMA

Int Cl (Ed.6): H03B 1/00,5/00,5/02,5/04,5/08,5/12,5/18

Other: ONLINE:WPI,EPODOC,PAJ

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	EP 0198550 A1 (PHILIPS)	1
A	US 5486794 (ROC)	1

X Document indicating lack of novelty or inventive step
Y Document indicating lack of inventive step if combined with one or more other documents of same category.

& Member of the same patent family

A Document indicating technological background and/or state of the art.
P Document published on or after the declared priority date but before the filing date of this invention.
E Patent document published on or after, but with priority date earlier than, the filing date of this application.

BEST AVAILABLE COPY